



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Docket No.: H1139.0109/P000
(PATENT)

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Technology Center 2600

In re Patent Application of:
Kimio Muramoto

Application No.: 10/035,192

Group Art Unit: N/A

Filed: January 4, 2002

Examiner: Not Yet Assigned

For: SYNCHRONIZATION TIMING
CORRECTING CIRCUIT AND METHOD

COMMUNICATION

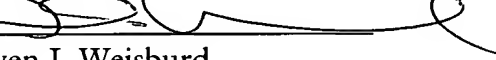
Commissioner for Patents
Washington, DC 20231

Dear Sir:

Please let us know when the above identified application is expected to be published.

Dated: July 19, 2002

Respectfully submitted,

By 
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